

L Number	Hits	Search Text	DB	Time stamp
1	222578	(design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)	USPAT	2004/09/03 14:26
2	277797	spreadsheet spread-sheet matrix (spread adj1 sheet)	USPAT	2004/09/03 14:26
3	127764	((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4))	USPAT	2004/09/03 14:26
4	197671	((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor))	USPAT	2004/09/03 14:26
5	1304	((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)))	USPAT	2004/09/03 14:13
6	710	((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spreadsheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor))))	USPAT	2004/09/03 14:14
7	39	((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spreadsheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)))) and default	USPAT	2004/09/03 14:24
8	39	(((((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spreadsheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)))) and default) and (spreadsheet spreadsheet matrix (spread adj1 sheet)))	USPAT	2004/09/03 14:24
9	475277	((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:27
10	603334	spreadsheet spread-sheet matrix (spread adj1 sheet)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:27
11	620799	(design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:28

12	251999	(spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:28
13	2172	((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:29
14	1098	((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:29
15	1098	((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:29
16	1368	cell with default	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:29
17	1	(((((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and (((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) with ((information data metadata specification description defin\$6) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)))) and (spreadsheet spread-sheet matrix (spread adj1 sheet))) and (cell with default)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:32
18	56	(spreadsheet spread-sheet matrix (spread adj1 sheet)) and ((design\$3 fabricat\$3 manufactur\$3 configur\$3 modeling) with (microprocessor microcontroller ic chip (integrated adj1 circuit) semiconductor)) and ((spreadsheet spread-sheet matrix (spread adj1 sheet)) with (creat\$3 generat\$3 form\$7 develop\$4)) and (cell with default)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/03 14:32